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*Action-based Model Checking*  
(and its applications to distributed,  
mobile, object-oriented systems)


**A Tutorial**

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**Introduction**

*This tutorial aims to present the possibilities offered by  
model checking tools for the verification of  
distributed, mobile, object-oriented systems,  
modelled by formalisms derived by **process algebras**.*

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## Outline of the tutorial

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### First part:

- *action-based logics interpreted over LTSs,*
- *discussion about their expressive power*
- *relations with process algebras.*
- *model checking algorithms and tools*

### Second part:

- *examples of applications*
- *infinite state systems*
- *extensions to mobility*
- *...and what about UML????*

## Model checking

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### Model Checking:

“checking that a given structure is a model for a given logical formula”.

### in practice:

- specification language based on finite state automata, process algebras, Petri nets
- model checking: proving that a formula in temporal logic (expressing a desired property) is satisfied by the automaton (or automata) that specifies the system

**Model Checking** (Clarke/Emerson, Queille/Sifakis- 1981)

ACTL  
Formula

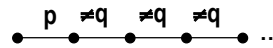
$AG([p] EF \langle q \rangle \text{true})$

MC

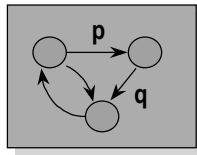
yes

algorithm

no



Counter-example



Finite-state model

**States or actions?**

CTL, the branching time temporal logic most used in model checking (EMC, SMV,...) is based on predicates on the states

CTL models are Kripke Structures, that is transition systems where the states are labelled by a set of atomic predicates

LTL, the linear time temporal logic used by SPIN has linear models: execution traces, sequences of states labelled by atomic predicates

Success of model checking techniques in hardware technology: a state is a bit vector

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## **States or actions?**

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Model checking techniques in software technology: states are variables'values.

But often many aspects of software (and the most interesting ones for the classes of reactive, concurrent, distributed software) are often seen as events, and event-based transition systems are often used as models

Process algebras have been recognized, since two decades, as a useful mean to model the behaviour of a system, abstracting from data and functions.

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## **States or actions?**

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In process algebras, a system is seen as a set of processes, and each process is characterised by the actions performed in the time by the process.

Hence a model checker focusing on actions, rather than states, is able to address the whole world of systems commonly modelled with process algebras, and with languages derived by process algebras. These systems include concurrent and distributed systems, mobile systems, and cover the behavioural aspects of object-oriented systems.

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ACTL Syntax -->  $\phi ::= \text{true} \mid \sim \phi \mid \phi \wedge \psi \mid \phi \vee \psi$   
 (state formulae)

$\phi ::= X\phi \mid X\phi \mid \phi \text{ U } \phi' \mid \phi \text{ U } \phi'$   
 (path formulae)

Action formulae -->  $\alpha ::= \alpha \mid \sim \alpha \mid \alpha \wedge \beta \mid \alpha \vee \beta$

- ACTL Semantics

The satisfaction of an ACTL formula is inductively defined over labelled transition systems

A labelled transition systems (LTS) is a 4-tuple  $A=(S, s_0, Act, \rightarrow)$ , where:

$S$  is a finite set of states;  $s_0$  is the initial state;

$Act$  is a finite set of observable actions;

$\rightarrow \subseteq S \times Act \times S$  is the transition relation between states.

We denote by  $s \xrightarrow{a} s'$ ,  $a \in Act$ , the transition from the state  $s$  to the state  $s'$  by executing  $a$ ; in particular,  $s \xrightarrow{a} s'$  indicates that a system in state  $s$  can perform a transition to state  $s'$  by executing the action  $a$ .

$D_a(s) = \{s' \mid s \xrightarrow{a} s'\}$  set of successors of  $s$

$\pi(s)$  is the set of paths starting from  $s$ ; a path is a sequence of successive transitions.

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## ACTL semantics

- $s \models_{TS} true$  always;
- $s \models_{TS} \phi \wedge \phi'$  iff  $s \models_{TS} \phi$  and  $s \models_{TS} \phi'$ ;
- $s \models_{TS} \phi \vee \phi'$  iff  $s \models_{TS} \phi$  or  $s \models_{TS} \phi'$ ;
- $s \models_{TS} \neg\phi$  iff not  $s \models_{TS} \phi$ ;
- $s \models_{TS} E\gamma$  iff there exists a path  $\pi \in \Pi(s)$  such that  $\pi \models_{TS} \gamma$ ;
- $s \models_{TS} A\gamma$  iff for all paths  $\pi \in \Pi(s)$ ,  $\pi \models_{TS} \gamma$ ;
- $\pi \models_{TS} X_{\chi}\phi$  iff  $|\pi| \geq 1$  and  $\pi(1) \in D_{\kappa(\chi)}(\pi(0))$  and  $\pi(1) \models_{TS} \phi$ ;
- $\pi \models_{TS} X_{\tau}\phi$  iff  $|\pi| \geq 1$  and  $\pi(1) \in D_{\{\tau\}}(\pi(0))$  and  $\pi(1) \models_{TS} \phi$ ;
- $\pi \models_{TS} \phi_{\chi}U\phi'$  iff there exists  $i \geq 1$  such that  $\pi(i) \models_{TS} \phi'$ , and for all  $1 \leq j \leq i-1$ :  $\pi(j) \models_{TS} \phi$  and  $\pi(j+1) \in D_{\kappa(\chi)}(\pi(j))$ ;
- $\pi \models_{TS} \phi_{\chi}U_{\chi'}\phi'$  iff there exists  $i \geq 2$  such that  $\pi(i) \models_{TS} \phi'$  and  $\pi(i) \in D_{\kappa(\chi')}(\pi(i-1))$ , and for all  $1 \leq j \leq i-1$ :  $\pi(j) \models_{TS} \phi$  and  $\pi(j) \in D_{\kappa(\chi)}(\pi(j-1))$ .

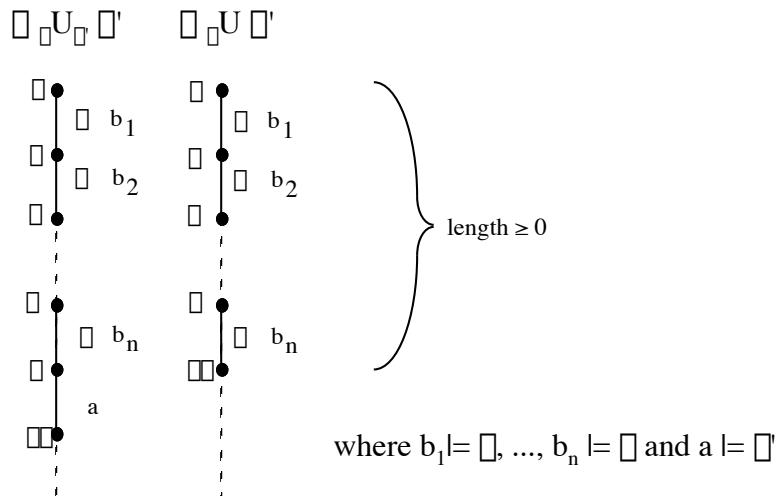
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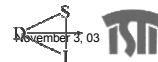
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## Action indexed Untils



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## Derived modalities

Several useful modalities can be defined, starting from the basic ones.

$EF\phi$  for  $E(\#U\phi)$ , and  $AF\phi$  for  $A(\#U\phi)$ ;

these are called the *eventually* operators

$EG\phi$  for  $\neg AF\neg\phi$ , and  $AG\phi$  for  $\neg EF\neg\phi$ ;

these are called the *always* operators

**Hennesy-Milner “weak” modalities, [ ] <>:**

$\langle a \rangle \square = E(\#_a U_a \square)$  (There exists a visible transition labelled by  $a$ )

$[a] \square = \neg \langle a \rangle \neg \square$  (For all transitions labelled by  $a, \dots$ )

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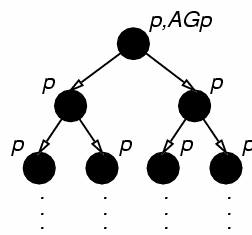


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## Universal derived modalities

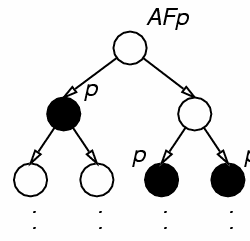
$AG p$

“globally  $p$ ”



$AF p$

“inevitably  $p$ ”



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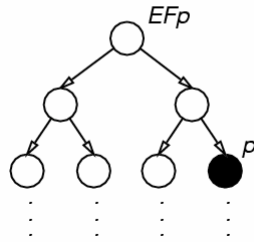


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# Existential derived modalities

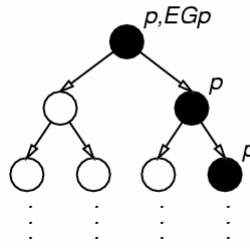
$EF p$

“possibly  $p$ ”



$EG p$

“?”



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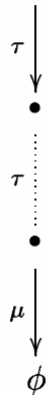
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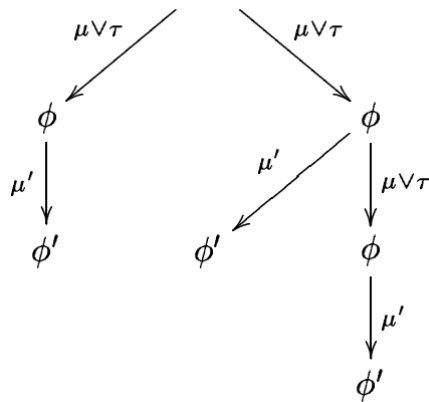
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# Other examples

$\langle \mu \rangle \phi$

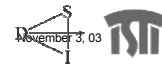


$A[\phi\{\mu\}U\{\mu'\}\phi']$



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## Safety and liveness properties

Classical distinction of properties of reactive systems:

- *Liveness properties* (something good eventually happens)
- *Safety properties* (nothing bad can happen)

$$AF EX_{\text{good}} \text{ true}$$

$$AG \neg EX_{\text{bad}} \text{ true} \quad (= \neg EF EX_{\text{bad}} \text{ true} )$$

## Relations between ACTL and process algebras

**A logic  $L$  is adequate with respect to an equivalence  $\equiv$ , if for every pair of processes  $q$  and  $q'$ ,  $q \equiv q'$  holds if and only if  $q$  and  $q'$  satisfy the same set of ACTL formulas.**

**The ACTL logic is adequate with respect to strong bisimulation equivalence on LTSs**

*Therefore minimization by strong bisimulation preserves ACTL formulae.*

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## Relations between ACTL and process algebras

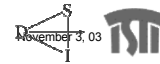
**Distinctive feature of ACTL and of Process algebras (vs. CTL/Kripke Structures):  
explicitation of the unobservable action Tau**

*(in Kripke Structure internal moves are modelled by stuttering)*

**The ACTL-X (ACTL without next) logic is adequate with respect to branching bisimulation equivalence on LTSs**

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## Variants of ACTL and other action-based logics

"Machine readable" syntax

$E[\langle \Box \rangle U \langle \Box \rangle]$  instead of  $E(\Box \cup \Box \Box)$

$EX\langle \Box \rangle \text{true}$  instead of  $Ex\Box \text{true}$

Strong version of HML modalities:

$\langle a \rangle \Box = EX_a \Box$      $[a] \Box = \neg \langle a \rangle \neg \Box$

Unless [Meolic]

$E[\langle \Box \rangle W \langle \Box \rangle] = E[\langle \Box \rangle U \langle \Box \rangle] \mid EG \langle \Box \rangle$

$A[\langle \Box \rangle U \langle \Box \rangle] = A[\langle \Box \rangle W \langle \Box \rangle] \ \& \ AF \langle \Box \rangle$

Eventually an action

$EF\langle \Box \rangle = E[\text{true}\{ \text{true} \} U \langle \Box \rangle]$

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## Variants of ACTL and other action-based logics

Expressive power of ACTL w.r.t. CTL:  
*CTL have more expressive power than ACTL  
(CTL can predicate over conjunctions of  
actions labeling a transition)*

Expressive power w.r.t. mu-calculus:  
*ACTL properties can be expressed in mu-  
calculus*

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## Variants of ACTL and other action-based logics

$\square$ -ACTL is an extension with a fixed point operator of ACTL

$\square$ -ACTL embeds the idea of "evolution in time by actions"

$\square ::= \text{true} \mid \sim \square \mid \square \& \square \mid E \square \mid A \square \mid \langle a \rangle \square \mid \square Y. \square (Y)$

$\square ::= X\{a\}\square \mid T \square \mid \square \{a\} U \{a'\} \square' \mid \square \{a\} U \square'$

The satisfaction of a  $\square$ -ACTL formula is defined inductively over a Labelled Transition System

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## Relations between ACTL and process algebras

- $\square$ -ACTL is adequate w.r.t. strong bisimulation  $\square$

*A logic L is adequate with respect to  $\square$  if for every pair of processes q and q',  $q \square q'$  holds if and only if q and q' satisfy the same set of ACTL formulas.*

- $\square$ -ACTL is able to express safety, liveness and cyclic properties of concurrent systems.
- $\square$ -ACTL has the same expressive power than  $\square$ -calculus
- $\square$ -ACTL is expressive w.r.t. strong bisimulation.

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## Basic model checking algorithm

We present a model checking algorithm for (a subset of) ACTL, directly derived by the Clarke-Emerson-Sistla 1986 algorithm

The algorithm proceeds visiting the automaton and labelling each state with the set of subformulae of the formula to be checked, that are satisfied in that state (*following the satisfaction relation  $\models$  used for defining the semantics of the logic*).

The subset considered is composed of state formulae:

$$P := \text{true} \mid \sim P \mid EX\{\text{act}\}P \mid E[P\{\text{act}\}U P]$$

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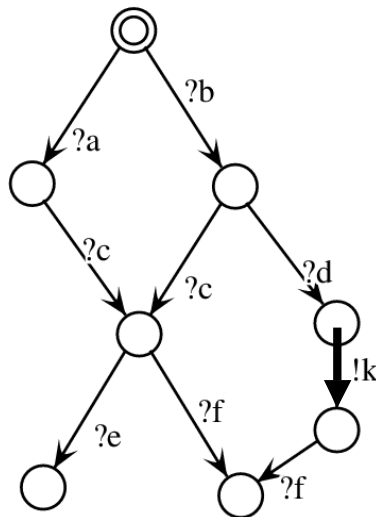
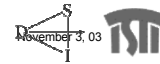


## (Explicit) Model checking algorithm

```

for i = 1 to length(p0)
  for each subformula p of p0 of length i
    case on the form of p
      p = true /* nothing to do */
      p = q and r: for each s in S
                    if q in L(s) and r in L(s) then add q and r to L(s)
                    end
      p = ~q:       for each s in S
                    if q in L(s) then add ~q to L(s)
                    end
      p = EX{a}q:   for each s in S
                    if (for some t in S, s-a->t, q in L(t)) then add EX{a}q to L(s)
                    end
      p = E[q {a}U r]: for each s in S
                      if r in L(s)
                        then add E[q {a}U r] to L(s)
                      end
                      for j = 1 to card(S)
                        for each s in S
                          if q in L(s) and (for some in S, s-a->t or s-[]->t, E[q {a}U r] in L(t))
                            then add E[q {a}U r] to L(s)
                          end
                        end
                      end
                    end
      end of case
    end
  end
end

```



Looking for a "bad" transition



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**Formula expressing:  
there exists no path that executes the bad transition.**

$$\sim \text{EF EX}\{!k\} \text{ true}$$

Length = 2

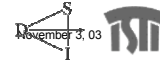
Length = 3

Length = 4

Note that  $\text{EF } p = \text{E}[\text{true}\{\text{true}\}\text{U } p]$

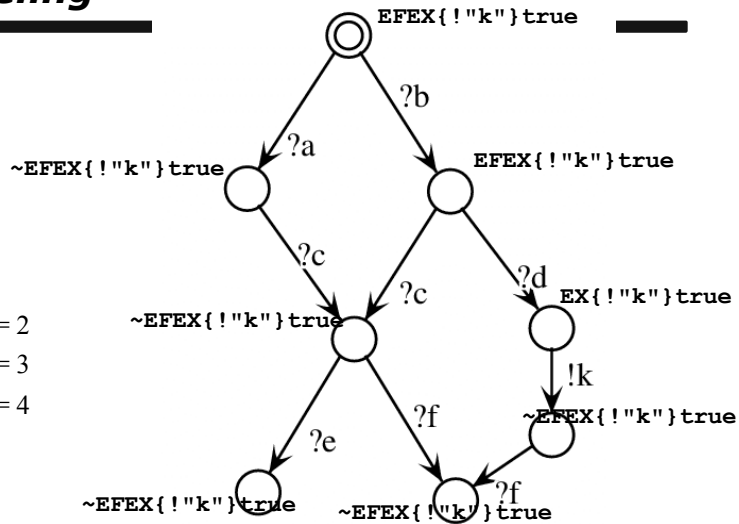
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### Labeling



Length = 2

Length = 3

Length = 4

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## Counterexample

- The *labeling* algorithm permits, in the case of negative result, to find out the reason of the result, since it is possible to find all those states that do not verify some significant subformula, and hence contribute to the failure of the verification;

in general, model-checking algorithms are able to provide a *counterexample*: for example, a path in the model that does not verify the (sub)formula.  $\square$

## Counterexample generation with AMC

```
top > load "/home/fantechi/jackov/Tzeroprimo.fc2"
Taking input from /home/fantechi/jackov/Tzeroprimo.fc2...
time: (user: 0.00 sec, sys: 0.00 sec)
```

```
top > eval
Evaluate mode. Graph 0

|= ~EF EX{!k} true

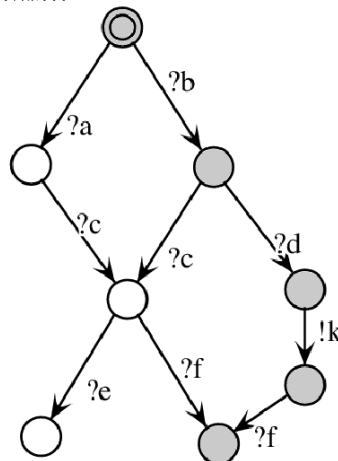
The formula is FALSE in state 0 time: (user: 0.00
```

```
|= why
why: (~ EF EX {!"k"} true) false 0

0 : (EF EX {!"k"} true) (1) is true
```

```
choice ?
|= why
why: (EF EX {!"k"} true) true 0
```

```
0 :
|
| labelled by : ?1
2 :
|
| labelled by : ?3
4 : (EX {!"k"} true) (1) is true
```



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## Computational Complexity

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Computational Complexity of this algorithm is in the worst case (Until formulae):

$$O(\text{length}(p_0) * \text{card}(S) * (\text{card}(S) + \text{card}(R)))$$

where  $\text{card}(S)$  is the number of states and  $\text{card}(R)$  is the number of transitions.

An immediate optimization in the search produces a complexity:

$$O(\text{length}(p_0) * (\text{card}(S) + \text{card}(R))) \square$$

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## State space explosion

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Though the explicit model checking algorithms are linear in the state space, is this space that often have a challenging size.

Indeed, in concurrent systems, the state space grows *exponentially* with the number of independent processes.

The state space is explicitly represented (e.g. by a matrix representing the transition relation), so tools capacity is limited by memory (no more than one million states)

A symbolic representation of the state space has been adopted to address large state spaces.

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## Symbolic Model Checking

Method used by most “industrial strength” model checkers:  
uses boolean encoding for state machine and sets of states.

Can handle much larger designs – hundreds of state  
variables.

BDDs traditionally used to represent boolean functions.

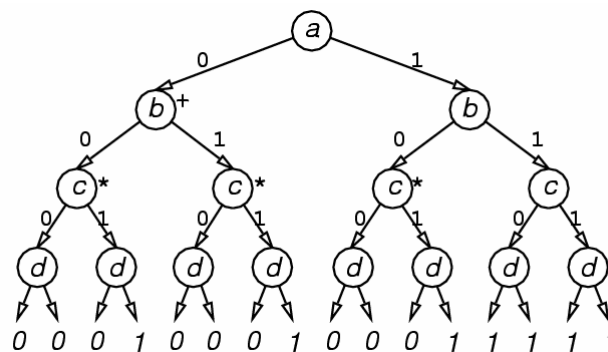


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## Ordered Decision Tree

Ordered decision tree for the function  $ab + cd$ :



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## Ordered Decision Tree

Rules for obtaining the function value:

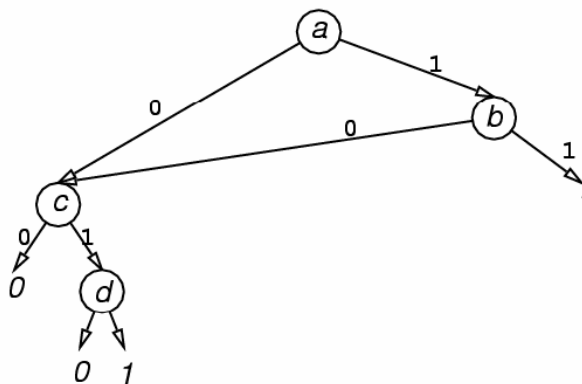
- Start at the root of the tree.
- At each node, take the “0” or “1” branch depending on the truth value of the given boolean variable.
- The leaf you reach gives the truth value of the function.

Requirement: the variables appear in the same order on any path from root to leaf. This requirement of a fixed order is very important for the results that follow.

Note: this tree is just an encoding of the truth table of the function.

## Binary Decision Diagram

Binary Decision Diagram for the function  $ab + cd$ :



## Binary Decision Diagram

The BDD is obtained from the ordered decision tree by the following steps:

- Combine isomorphic subtrees.
- Eliminate redundant nodes (nodes with identical children).

## BDD encoding

How to represent state-transition graphs with Ordered Binary Decision Diagrams:

Assume that states are encoded by  $n$  boolean variables

$v_1, v_2, \dots, v_n$ .

Possible actions are encoded by  $m$  boolean variables

$a_1, a_2, \dots, a_m$

The Transition relation  $T$  will be given as a boolean formula in terms of the state and action variables:

$T(v_1, \dots, v_n, a_1, a_2, \dots, a_m, v'_1, \dots, v'_n)$

Which gives true if there is a transition leaving the current state  $v_1, \dots, v_n$ , labelled by action  $a_1, a_2, \dots, a_m$ , and with next state  $v'_1, \dots, v'_n$

**Now convert  $T$  to a OBDD!!**

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Similar encodings can be done for ACTL formulae, through encoding boolean operators, quantifiers, and fixed points.

Model checking then amounts to a few implication-like boolean operators on the BDDs of the LTS and of the formula

The evaluation of this operator is also reduced to a BDD traversal

- very efficient in terms of memory occupation
- can handle millions of states easily

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## **Other techniques for dealing with State Explosion**

- Use of simmetries in the model
- Abstraction techniques
- Compositional Reasoning (Assume/Guarantee)
- Abstract Interpretation
- LTS Minimization by equivalence
- Partial Order Reduction
- Model checking "On the Fly"(local model checking):  
*lo stato globale dell'automa non viene costruito completamente prima di applicare l'algoritmo di etichettamento, ma vengono via via generate solo le regioni dell'automa che sono strettamente necessarie a verificare la formula*

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## ACTL model checkers

AMC - *Explicit model checker for ACTL inside the JACK verification environment (ISTI- CNR)*

FMC - *On the fly model checker for ACTL inside the JACK verification environment (ISTI- CNR)*

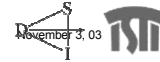
Evaluator - *inside the CADP verification environment (INRIA): on-the-fly model-checking of regular alternation-free mu-calculus formulas on LTSSs. Regular alternation-free mu-calculus allows direct encodings of ACTL .*

EST - *Efficient Symbolic Tool: BDD- based model checker for ACTL (University of Maribor)*

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## INFINITE STATE PROCESSES

### Non-finiteness due to:

- Data, variables,
- inherent behaviour of the system under specification: I.e. Context-free or more.

we do not build an abstract (with respect to values) model on which the properties are proved.

we define a suitable chain of finite labeled transition systems based on the operational semantics: this allows us to choose the approximation level case by case.

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• **CCS Syntax**  $--> p ::= \square.p \mid \text{nil} \mid p+p \mid p \mid p \mid p \setminus A \mid x \mid p[f]$

• **CCS Semantics**  $-->$  Operational semantics  
Labelled transition systems  
(finite-non finite state)  
Behavioural equivalences

**Example BAG:**

$X = p1.(g1.\text{nil} \mid X) + p2.(g2.\text{nil} \mid X)$  *non finite state*

$p1, p2$  insertion of data values 1,2  
 $g1, g2$  extraction of data values 1,2

**Finite ACTL**

We say that  $\square$  is a finite ACTL formula if  $\square$  is an ACTL formula without until operators.

**Positive ACTL**

We say that  $\square$  is a positive ACTL formula if  $\square$  is an ACTL formula without negations (we admit negations in the action formulae).

The positive finite ACTL subset is defined analogously.

**Depth of a finite formula**

If  $\square$  is a finite ACTL formula, the depth of  $\square$  is the maximum number of nested next operators occurring in  $\square$

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### Formulae preserved by preorders

A preorder  $\sqsubseteq$  over  $T$  preserves a formula  $\phi$  if:

**$(TS_1 \models \phi$  and  $TS_1 \sqsubseteq TS_2$ ) implies  $TS_2 \models \phi$**

$\sqsubseteq_s$ : Simulation preorder does not preserve universal positive formulae

Example:

Each path starts with an action  $a$ :  **$AX_a$  true**

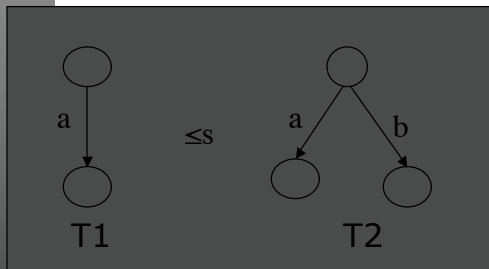
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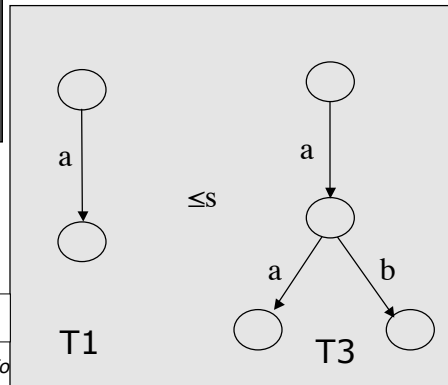


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### Simulation preorder and ACTL properties



**T1 =  $AX_a$  true**  
**T2  $\neq$   $AX_a$  true**  
**T3 =  $AX_a$  true**



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## Branching complete-simulation

Let  $TS_1$  and  $TS_2$  be LTSs and let  $s_1 \in S_1$  and  $s_2 \in S_2$ .

We say that  $s_2$  *BC-simulates*  $s_1$  if there exists a strong *BC-Simulation* that relates  $s_1$  and  $s_2$ .  $R \subseteq S_1 \times S_2$  is a strong BC-simulation if  $(s_1, s_2) \in R$ :

- either  $s_1 = \perp$  or
- 1)  $s_1 - a \rightarrow s_1'$  implies  $s_2 \rightarrow s_2'$  and  $(s_1', s_2') \in R$
- 2)  $s_2 - a \rightarrow s_2'$  implies  $s_1 \rightarrow s_1'$  and  $(s_1', s_2') \in R$

$TS_2$  *BC-simulates*  $TS_1$  ( $TS_1 \preceq_{bc} TS_2$ ) if a branching complete simulation  $R$  exists such that  $(s_{01}, s_{02}) \in R$

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## Bc-simulation

$\preceq_{bc}$  preserves the whole positive fragment of ACTL:

*Proposition*

Let  $TS_1$  and  $TS_2$  be LTSs and let  $\phi$  be a Positive ACTL formula:

- $(TS_1 \models \phi \text{ and } TS_1 \preceq_{bc} TS_2) \text{ implies } TS_2 \models \phi$

$\preceq_{bc}$  is more adequate than  $\preceq_s$  for proving properties.

The set of formulae preserved by  $\preceq_s$  is strictly included in the set of formulae preserved by  $\preceq_{bc}$ .

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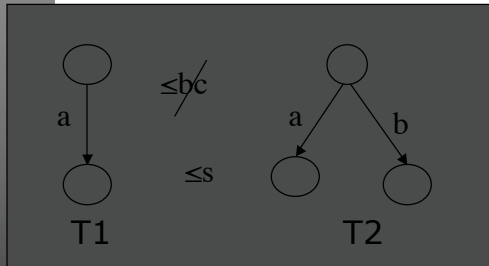
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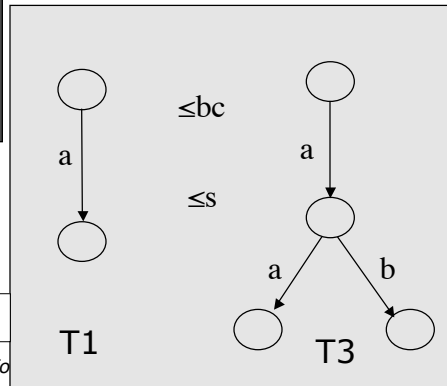


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## BC-Simulation and ACTL properties



$T1 = |AXa \text{ true}$   
 $T2 \neq |AXa \text{ true}$   
 $T3 = |AXa \text{ true}$



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## Model checking by approximation chains

To model-check if a CCS description of a system enjoys properties specified as ACTL formulae, its LTS is usually first built

**This does not work when the system has a infinite-state representation**

**Our approach** for proving the validity of properties on infinite-state system **consists in checking** the properties **on the elements of an "increasing" approximation chain of finite transition systems, until the properties are verified.**

Thus, in general, this is a semi-decision procedure for proving properties.

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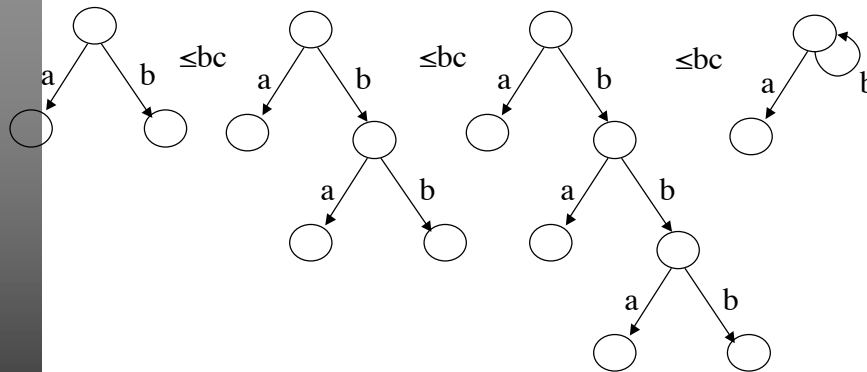
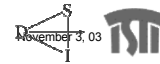


## Bc-simulation and approximation chains

Using the Bc-simulation preorder is it possible to define chains of finite LTSs that approximates the behavior of possible infinite-state process.

Each element of the chain is finite and each  $Ts_i$  is completely included in  $Ts_{i+1}$ .

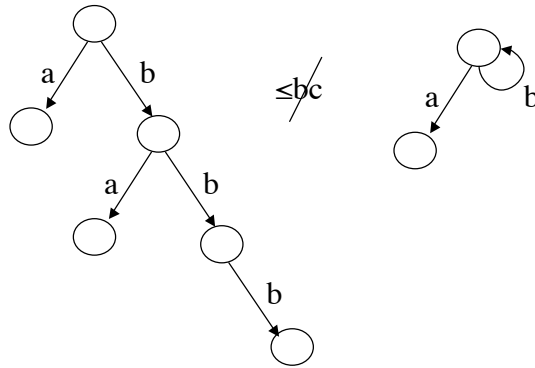
Approximation chains can be used to define a decision procedure to verify the satisfiability of ACTL formulae on infinite-state processes



Approximating chains

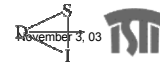


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### Example: BAG

$$X = p1. (g1.nil \mid X) + p2.(g2.nil \mid X)$$

- The bag is not a set, therefore there exists a computation path on which it is possible to get twice the same value from the bag consecutively:  $EF\langle g\_1 \rangle \langle g\_1 \rangle \text{ true}$
- It is possible, on some (but finitely many) states to do a *put* immediately followed by a *get* action:  $EFE G\langle p\_1 \rangle \langle g\_1 \rangle \text{ true}$
- There exists a computation path on which it is possible to do infinitely often *put* actions:  $EGAF( E(\text{true} \text{ true} U_{p1|p2} \text{true}) )$ .
- It is always possible to perform a *put* action:  $AG EX_{p1|p2} \text{true}$

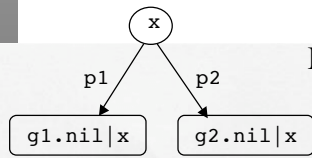
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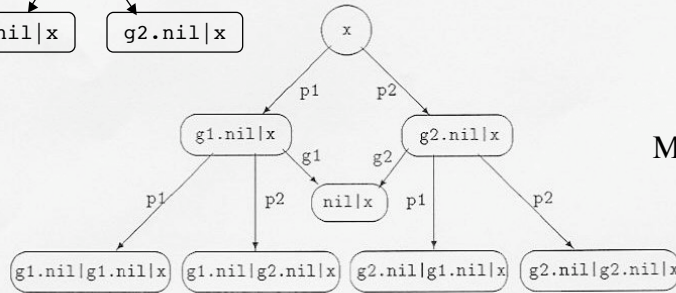
$$X = p1. (g1.nil | X) + p2.(g2.nil | X)$$



$M_1(X)$

$M_1(X) \not\models EF\langle g_1 \rangle \langle g_1 \rangle \text{ true}$

$M_2(X) \models EF\langle g_1 \rangle \langle g_1 \rangle \text{ true}$



$M_2(X)$

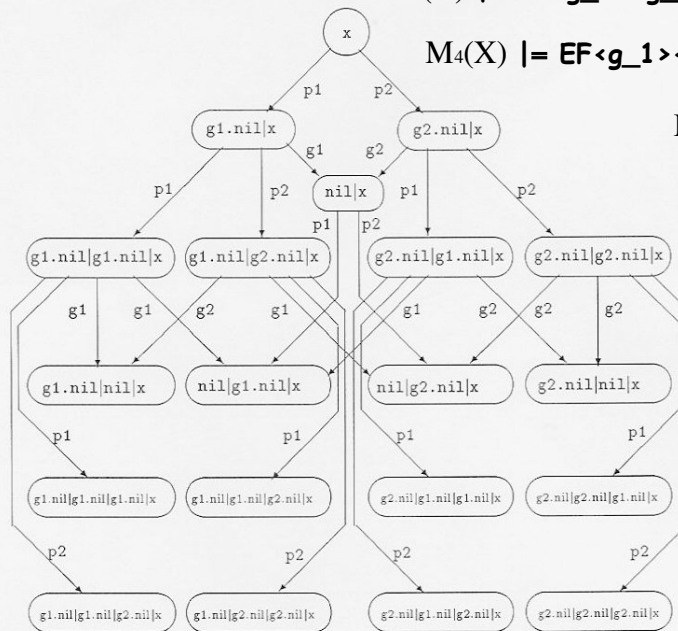
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$M_3(X) \not\models EF\langle g_1 \rangle \langle g_1 \rangle \text{ true}$

$M_4(X) \models EF\langle g_1 \rangle \langle g_1 \rangle \text{ true}$



$M_3(X)$

**F****Example: BAG**

$$X = p1. (g1.nil \mid X) + p2.(g2.nil \mid X)$$


---

**Properties,**

**EFEG** $\langle p_1 \rangle \langle g_1 \rangle$  true,

**EGAF**( E (true  $\text{true} \text{U}_{p1 \mid p2}$  true),

**AG**  $\text{EX}_{p1 \mid p2}$  true

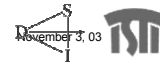
are not verified by any  $M_i$  for each  $i$

Their satisfiability implies detecting a cycle in the transition system.

By induction on the length of the chain  $M_i$  it can be proved that no cycle belongs to the transition systems of the chain.

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**F**

## SS approximations

---

We have used another way of approximating systems which is based on a different operational semantics, which **allows us to prove a greater** set of properties than those proved by  $M_i$ , **the SS semantics** [DI93].

It is **more abstract than SOS**, since the SS rules **have built in some behavioral equivalence axioms**, i.e. they accomplish some simplifications on the terms during the derivations.

In this way it is possible to obtain **more succinct LTSs** than those obtainable with the standard SOS rules.

**Sometimes, the transition system obtained by using the SS rules is finite while the SOS one is not.**

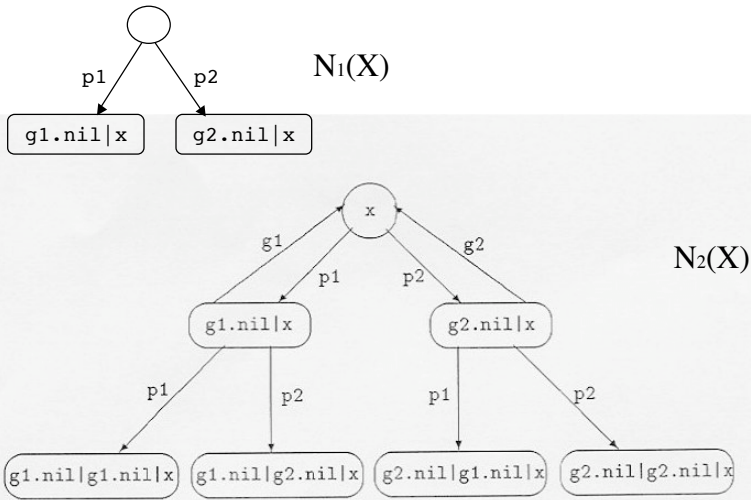
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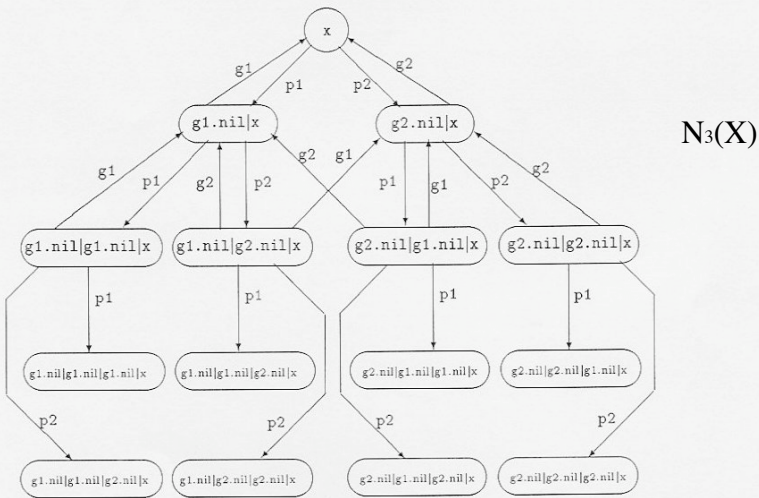




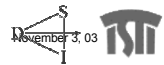
## SS approximations chains



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**F****Example: BAG**

$$X = p1. (g1.nil \mid X) + p2.(g2.nil \mid X)$$

Properties,

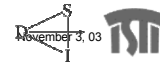
$EF\langle g\_1 \rangle \langle g\_1 \rangle \text{ true}$   
is verified by N3

$EFE G\langle p\_1 \rangle \langle g\_1 \rangle \text{ true}$ ,  
 $EGAF( E(\text{true}_{\text{true}} U_{p1|p2} \text{true}) )$ ,  
are verified by N2

$AG EX_{p1|p2} \text{ true}$   
is not verified by any  $N_i$  for each  $i$

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**F** **$\pi$  logic: an extension of ACTL for mobility**

*Mobility is introduced, referring to the  $\pi$  calculus formalism as a basic process algebra able to express the typical issues of mobility.*

*Model checking tools operating over pi-calculus agent are offered by the Mobility Workbench (MWB) and by the HD-Automata Laboratory (HAL). The core of HAL are the HD-automata: they are used as a common format for the various history-dependent languages. The HAL environment includes modules which support verification of behavioral properties of pi-calculus agents expressed as formulae of suitable temporal logics.*

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## Variants of ACTL: Mobile systems

$\square$ -logic syntax  $\rightarrow$

$\square ::= \text{true} \mid \sim \square \mid \square \square \mid \square X\{\square\} \mid \langle \square \rangle \square \mid \square F \square \mid \square F\{\square\}$

$\square ::= \text{tau} \mid x!y \mid x!(y) \mid x?y$

$\square X\{\square\}$  strong next

$\langle \square \rangle$  weak next

$\square F \square$  eventually

$\square F\{\square\}$  eventually guarded by  $\square$

As usual  $\square \square$ ,  $AG \square$  can be defined by duality

$\square$ -logic is adequate with respect to strong early bisimulation equivalence

## $\square$ -logic semantics

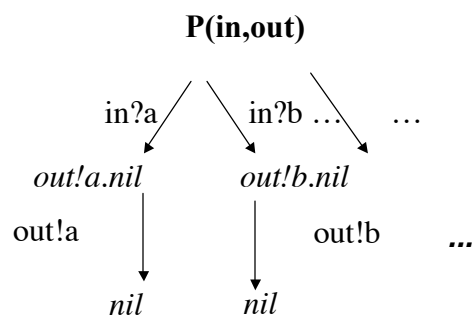
- $P \models \text{true}$  holds always;
- $P \models \sim \phi$  if and only if not  $P \models \phi$ ;
- $P \models \phi \ \& \ \phi'$  if and only if  $P \models \phi$  and  $P \models \phi'$ ;
- $P \models EX\{\chi\}\phi$  if and only if:
  - case  $\chi = \mu$   
there exists  $P'$  such that  $P \xrightarrow{\mu} P'$  and  $P' \models \phi$ ;
  - case  $\chi = \sim \mu$   
there exists  $P'$  such that  $P \xrightarrow{\mu'} P'$  and  $\mu' \neq \mu$  and  $P' \models \phi$ ;
  - case  $\chi = \bigvee_i \mu_i$   
there exist  $P', i$  such that  $P \xrightarrow{\mu_i} P'$  and  $P' \models \phi$ ;
- $P \models EF \phi$  if and only if there exist  $P_0, \dots, P_n$  and  $\mu_1, \dots, \mu_n$ , with  $n \geq 0$ , such that  $P = P_0 \xrightarrow{\mu_1} P_1 \dots \xrightarrow{\mu_n} P_n$  and  $P_n \models \phi$ .
- $P \models EF\{\chi\}\phi$  if and only if there exist  $P_0, \dots, P_n$  with  $n \geq 1$ , such that:
  - case  $\chi = \mu$   
 $P = P_0 \xrightarrow{\mu} P_1 \dots \xrightarrow{\mu} P_n$  and  $P_n \models \phi$ ;



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**$P(in,out) ::= in?(x). out! x nil$**

$x, in, out \in N$   
 $N$  infinite sets of names  
 $in, out$ : channels  
 $x$ : place holder



**THE SEMANTICS MODEL OF P IS:  
INFINITE STATE  
INFINITE BRANCHING**

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**JACK for MOBILITY  
HD -automata**

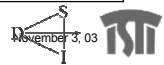
$\lambda$ -calculus requires an infinite number of states also for very simple agents. The creation of a new name gives rise to an infinite set of transitions: one for each choice of the new name.

In HD-automata names appear explicitly in states, transitions and labels (local names) . Local names do not have a global identity.

In this way, for instance, a single state of the HD-automaton can be used to represent all the states of a system that differ just for a bijective renaming.

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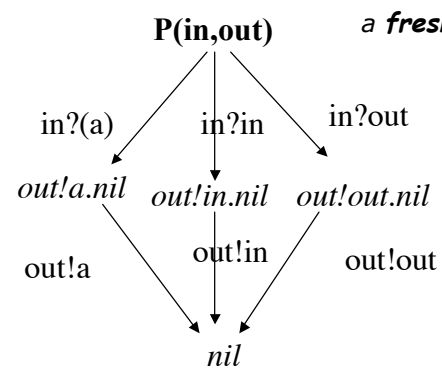


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## FROM HD-AUTOMATA TO LTSs

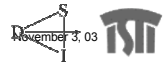
$P(\text{in}, \text{out}) ::= \text{in?}(x). \text{out! } x \text{ nil}$

*in, out are the active names of P  
a fresh name*



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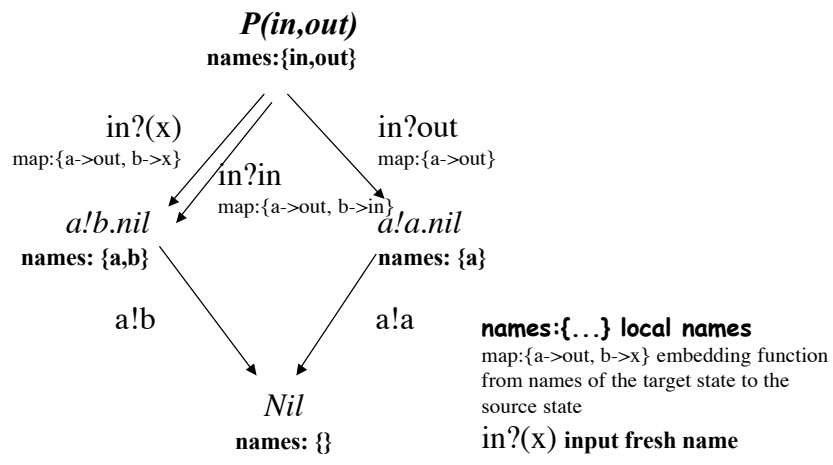
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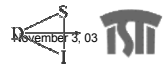
## FROM $\lambda$ -CALCULUS TO HD-AUTOMATA

$P(\text{in}, \text{out}) ::= \text{in?}(x). \text{out! } x \text{ nil}$



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### A translation function exists from $\square$ -logic to ACTL

**soundness** : a  $\square$ -logic formula is satisfied by a  $\square$ -calculus agent P if and only if the finite state ordinary automaton associated with P satisfies the corresponding ACTL formula

The translation of a formula is thus not unique, but depends on the agent P. Specifically, it depends on the set S of the fresh names of the ordinary automaton associated with the agent P.

- $\mathcal{T}_S(true) = true$
- $\mathcal{T}_S(\phi_1 \& \phi_2) = \mathcal{T}_S(\phi_1) \& \mathcal{T}_S(\phi_2)$
- $\mathcal{T}_S(\sim \phi) = \sim \mathcal{T}_S(\phi)$
- $\mathcal{T}_S(EX\{\chi\}\phi) = \bigvee_{\mu' \in \mathcal{T}_S(\chi)} EX\{\mu'\}\mathcal{T}_S(\phi\theta)$  where  $\theta = \{y'/y\}$  if  $bn(\chi) = y$  and  $bn(\mu') = y'$
- $\mathcal{T}_S(EF\phi) = EF\mathcal{T}_S(\phi)$
- $\mathcal{T}_S(EF\{\chi\}\phi) = E[true \bigvee_{\mu' \in \mathcal{T}_S(\chi)} \mu' U \mathcal{T}_S(\phi)]$

where:

- $\mathcal{T}_S(\mathbf{tau}) = \{\mathbf{tau}\}$
- $\mathcal{T}_S(x!y) = \{x!y\}$

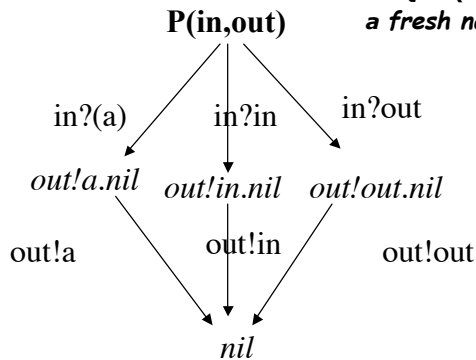
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### Model checking facilities

$P(\text{in}, \text{out}) ::= \text{in?}(x). \text{out! } x \text{ nil}$

$EX \{ \text{in?}u \} EX \{ \text{out!}u \} \text{ true } (\square\text{-logic})$

$EX \{ \text{in?}(a) \} EX \{ \text{out!}a \} \text{ true } (\text{ACTL})$   
*a fresh name*



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### JACK for MOBILITY

The generation of the ordinary automaton associated with a pi-calculus agent consists of two stages.

The first stage constructs an intermediate representation of agent's behaviour as HD-automaton

The second stage builds the ordinary automaton starting from the HD-automaton.

The generation of the ordinary automaton has been split into these two steps to achieve modularity in the structure of the verification environment and allows a more efficient implementation of the second translation step.

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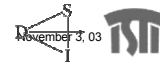
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## The HAL environment: an overview

HAL is written in C++ and compiles with the GNU C++ compiler (the GUI is written in Tcl/Tk).  
It is currently running on SUN stations (under SUN-OS) and on PC stations (under Linux).

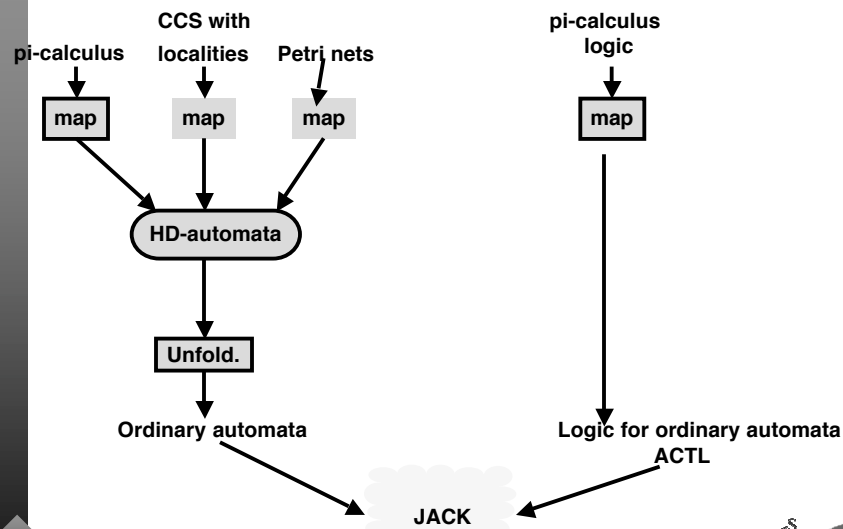
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## The HAL environment: an overview



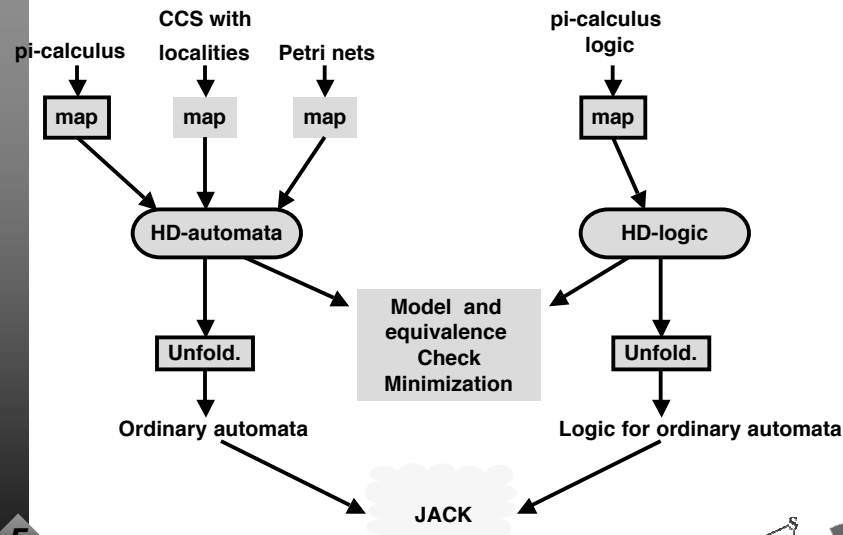
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## The HAL environment: an overview



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## HAL applications handover protocol

The specification, *GSM*, describing the core of system is basically composed by four modules:

- a Mobile Station *MS*, mounted in a car moving through two different Geographical areas (cells), that provides services to an end user;
- a Mobile Switching Centre *MSC*, that is the controller of the radio communications within the whole area composed by the two cells;
- the Base Station modules *BSa* and *BSp*, that are the interfaces between the Mobile Station;
- the Mobile Switching Centre.

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```

define Car(talk,switch,out) =
  talk?(msg).out!msg.Car(talk,switch,out) +
  switch?(t).switch?(s).Car(t,s,out)

define Base(talkcentre,talkcar,give,switch,alert) =
  talkcentre?(msg).talkcar!msg.
  Base(talkcentre,talkcar,give,switch,alert)
+
  give?(t).give?(s).switch!t.switch!s.give!give.
  IdleBase(talkcentre,talkcar,give,switch,alert)

define IdleBase(talkcentre,talkcar,give,switch,alert) =
  alert?(empty).Base(talkcentre,talkcar,give,switch,alert)

define Centre(in,tca,ta,ga,sa,aa,tcp,tp,gp,sp,ap) =
  in?(msg).tca!msg.Centre(in,tca,ta,ga,sa,aa,tcp,tp,gp,sp,ap)
+
  tau.ga!tp.ga!sp.ga?(empty).ap!ap.Centre(in,tcp,tp,gp,sp,ap,tca,ta,ga,sa,aa)

define GSM(in,out) =
  (tca)(ta)(ga)(sa)(aa)(tcp)(tp)(gp)(sp)(ap) | (Car(ta,sa,out), Base(tca,ta,ga,sa,aa),
  IdleBase(tcp,tp,gp,sp,ap), Centre(in,tca,ta,ga,sa,aa,tcp,tp,gp,sp,ap))

```

There are two kinds of correctness checking that can be performed by exploiting HAL facilities.

One is the checking that the specification of the The Handover Protocol is (early) bisimilar to a more abstract service specification, that models the intended behaviour of the system.

The other is the (model) checking of some interesting properties.

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## HAL applications

a more abstract representation of GSM

```

define S0(in,out) =
  in?(v). S1(in,out,v)
  +
  tau. S0(in,out)

define S1(in,out,v1) =
  in?(v). S2(in,out,v1,v)
  +
  out!v1. S0(in,out)
  +
  tau. out!v1. S0(in,out)

define S2(in,out,v1,v2) =
  in?(v). S3(in,out,v1,v2,v)
  +
  out!v1. S1(in,out,v2)
  +
  tau. out!v1. out!v2. S0(in,out)

define S3(in,out,v1,v2,v3) =
  out!v1. S2(in,out,v2,v3)

define GSMbuffer(in,out) = S0(in,out)

```

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## HAL applications

Some logic formulae, describing that the protocol is reliable, have been expressed  $\square$ -logic formulae.

no messages are lost: that is whenever a message is received from the external environment through an input channel then it will be eventually retransmitted to the end user via the output channel.

$$\mathbf{AG}([\text{in?}m]\mathbf{EF}\langle\text{out!}m\rangle\mathbf{true})$$

in-order delivery: whenever three messages are sequentially received in sequence through an input channel then the first message can be soon retransmitted to the end user through the output channel.

$$\mathbf{AG}[\text{in?}m][\text{in?}n] \sim \mathbf{EF} \{ \sim \text{out!}n \} \mathbf{EX} \{ \text{out!}m \} \mathbf{true}$$

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We summarize the figures (states, transitions and times) of the different steps of a typical session of verification for the handover protocol (GSM spec)

buildHD GSM.pi	506	745	37.52 sec.
reduceHD-red GSM.hd	245	484	1.19 sec.
buildFC2	545	1062	1.54 sec.
Minimize automaton	49	91	3.45 sec.
Model checking			6 sec.
	»	States Trans.	Time

*A most recent approach to the specification of distributed object-oriented processes is by using the proper UML diagrams, such as the state diagrams, to express the behavioural aspects of the overall design. Again, this tutorial will address the issue of how action-based model checking can be upgraded to deal with UML state diagrams: the UMC model checker will be presented as an example.*

Evolution formulae:

$$\square ::= tt \mid [\text{target.}] \text{event}[(\text{args})] \mid \square \mid \square \square \mid \square \square$$

$\square$ -ACTL<sup>+</sup> formulae:

$$\begin{aligned} \square ::= & \text{true} \mid \square_1 \square_2 \mid \square \square \mid \\ & \text{EX}_{(\square)} \square \mid \text{AX}_{(\square)} \square \mid \text{EF}_{(\square)} \square \mid \text{EF} \square \mid \min Y: \square(Y) \mid Y \mid \\ & \text{ASSERT}(\text{VAR}=\text{value}) \end{aligned}$$

Doubly Labelled Transition System:  $(Q, q_0, \text{Act}^* + \{\text{tau}\}, R, \mathcal{L})$

$(Q, q_0, \text{Act}^* + \{\text{tau}\}, R)$  is a LTS

$\mathcal{L}$  is a labelling function  $\mathcal{L}: Q \rightarrow \mathcal{AP}$

$\mathcal{AP}$  is a finite set of atomic propositions

( typically of the form VAR=value)

## $\Box$ -ACTL<sup>+</sup> semantics 1

transition label  $\models$  evolution formula  $\models$  is the Satisfaction relation

$\Box \models tt$	holds always
$\Box \models \Box \Box$	iff not $\Box \models \Box \Box$
$\Box \models \Box_1 \Box \Box_2$	iff $\Box \models \Box_1$ and $\Box \models \Box_2$
$\Box \models \Box$	iff $\Box = \text{tau}$
$\Box \models [\text{target.}] \text{event}[(\text{args})]$	iff $\Box = e_1; \dots; e_n$ $1 \leq n$ and $\Box_i, 1 \leq i \leq n: e_i = \text{target.event}(\text{args})$

## $\Box$ -ACTL<sup>+</sup> semantics 2

state  $\models$  formula  $\models$  is the Satisfaction relation

$q \models \text{ASSERT}(\text{VAR}=\text{value})$	iff $\text{VAR}=\text{value} \in \mathcal{L}(q)$
$q \models \text{true}$	holds always
$q \models \Box \Box$	iff not $q \models \Box \Box$
$q \models \Box_1 \Box \Box_2$	iff $q \models \Box_1$ and $q \models \Box_2$
$q \models \text{EX}_{\langle \Box \rangle} \Box$	iff $\exists q'$ tale che $q \xrightarrow{\Box} q'$ , $q' \models \Box$ , $\Box \models \Box$

$\square$ -ACTL<sup>+</sup> semantics 3

$q \models \text{AX}_{(\square)} \square$  iff  $\square q' : q \xrightarrow{\square} q'$ , and  
 $\square q' : q \xrightarrow{\square} q'$ ,  $q' \models \square$ ,  $\square \models \square$   
 $q_0 \models \text{EF} \square$  iff  $\square q_1 \dots q_n$ ,  $\square_1 \dots \square_n$ ,  $0 \leq n$  :  $q_n \models \square$   
 and  $\square i : 0 \leq i < n$ ,  $q_i \xrightarrow{\square_{i+1}} q_{i+1}$   
 $q_0 \models \text{EF}_{(\square)} \square$  iff  $\square q_1 \dots q_n$ ,  $\square_1 \dots \square_n$ ,  $0 \leq n$  :  $q_n \models \square$   
 and  $\square i : 0 \leq i < n$ ,  $q_i \xrightarrow{\square_{i+1}} q_{i+1}$ ,  $\square_i \models \square$   
 $q \models \min Y : \square(Y)$  iff  $\square n : 0 \leq n$   $\left( \square^n(\square \text{true}) \right)$ ,  
 where  $\square^0(Y) = \square(\square \text{true})$ ,  $\square^{n+1}(Y) = \square(\square^n(Y))$

 $\square$ -ACTL<sup>+</sup>

$\text{EX}_{\{\text{Chart.my\_event}\}} \text{true}$   
 in the current configuration the system can perform an evolution in which a state machine sends the signal my\_event to the state machine Chart.  
 $\text{EX}_{\{\text{my\_event}(3)\}} \text{true}$   
 in the current configuration the system can perform an evolution in which a state machine sends the signal my\_event(3) to some other state machine.  
 $\text{AG}((\text{EX}_{\{\text{my\_event}\}} \text{true}) \rightarrow \text{ASSERT}(\text{Chart.Status}=1))$   
 the signal my\_event can be sent, only when the object is in status 1.

- Development of Model checking techniques based on:

### *On the fly model checking UML State machines*

- we have defined a procedure which builds the model of a complex system starting from its component subsystems (a network) while evaluating a formula.
- a network represents a concurrent system as a collection of synchronized agents working in parallel.
- It is possible in this way to verify interesting properties also on systems for which the state explosion problem makes other verification tools inapplicable

### *On the fly model checking*

```

Evaluate (F: Formula, S: State) is
  if we have already done this evaluation and
    the result is available then
    return the already known result
  elseif we are already trying to evaluate F in S then
    return true or false depending on maximum or minimum
      fixed point semantics
  else
    Keep track of the fact that we are trying to evaluate F in S
    -- (e.g. push the pair (F,S) in a stack)
    for each sub-formula F'and
      next state S' which needs to be evaluated loop
      call recursively Evaluate (F' S');
      if the result of Evaluate (F' S') is sufficient
        to establish the result of evaluate (F,S) then
        exit from the loop;
      end if
    end loop
    -- (at this point we have in any case a final
    result)
    Keep track of the fact that we are
      no longer trying to evaluate F in S;
    (e.g. pop the pair (F,S) from the stack)
    Possibly keep track of the performed evaluation and result (e.g. push the triple (F, S, result) in a hash table)
    return the final result
  end if
end Evaluate;

```

## UMC assumptions

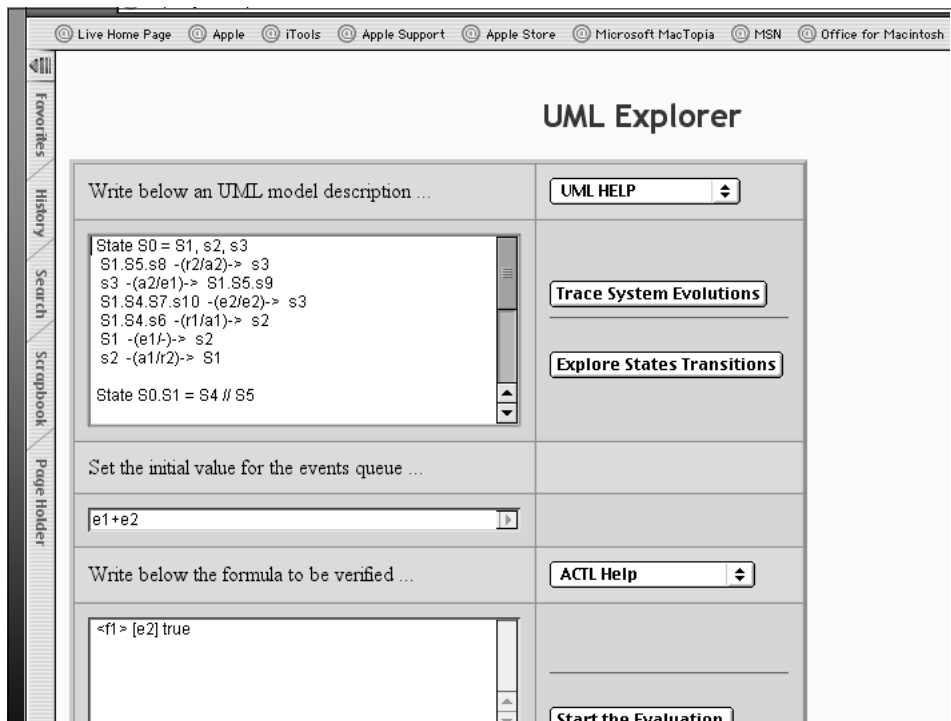
The whole sequence of actions constituting the actions part of statechart transition, is supposed to be executed as an indivisible atomic activity.

Given a model constituted by more than one state machine, a system evolution is constituted by any single evolution of any single state machine.

The propagation of signals inside a state machine and among state machines is considered instantaneous, and loss free.

The events queue associated with a state machine handles its events in a FIFO way.

The relative priority of a join transition is always well defined and statically fixed.



UML Explorer

Write below an UML model description ...

```
State S0 = S1, s2, s3
S1.S5.s8 -(r2/a2)-> s3
s3 -(a2/e1)-> S1.S5.s9
S1.S4.S7.s10 -(e2/e2)-> s3
S1.S4.s6 -(r1/a1)-> s2
S1 -(e1/-)-> s2
s2 -(a1/r2)-> S1

State S0.S1 = S4 // S5
```

Set the initial value for the events queue ...

e1+e2

Write below the formula to be verified ...

<f1 > [e2] true

UML HELP

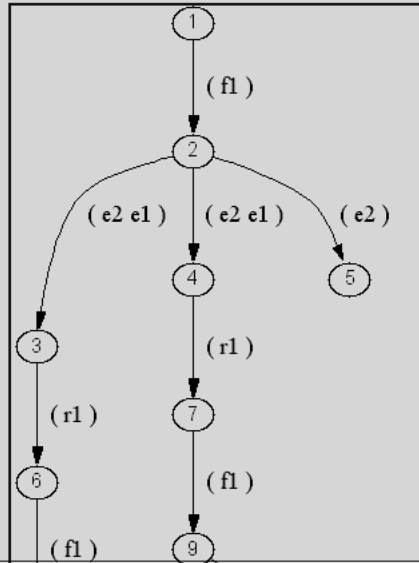
Trace System Evolutions

Explore States Transitions

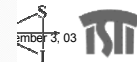
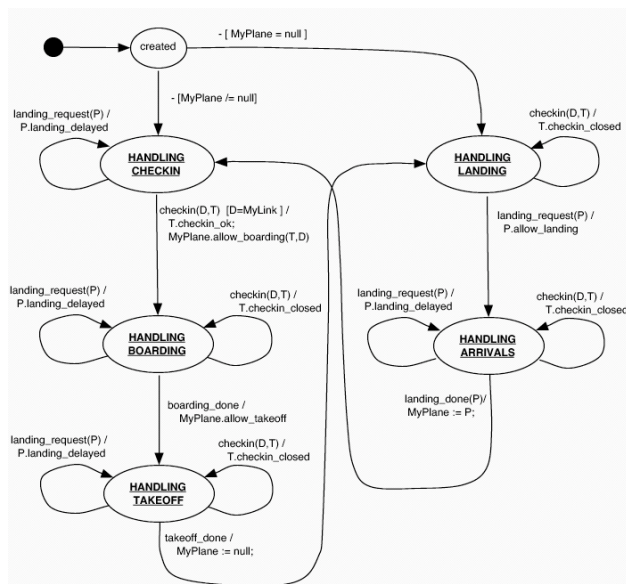
ACTL Help

Start the Evaluation

<b>Events Queue</b>	$\langle e1, e2 \rangle$
<b>Active States</b>	$\langle S0, S1, S4, s6, S0, S1, S5, s8 \rangle$
<b>Fireable Transitions</b>	1) $S0, S1, S4: (s6) \rightarrow (e1/f1) \rightarrow (S7)$
<b>Maximal Sets</b>	1

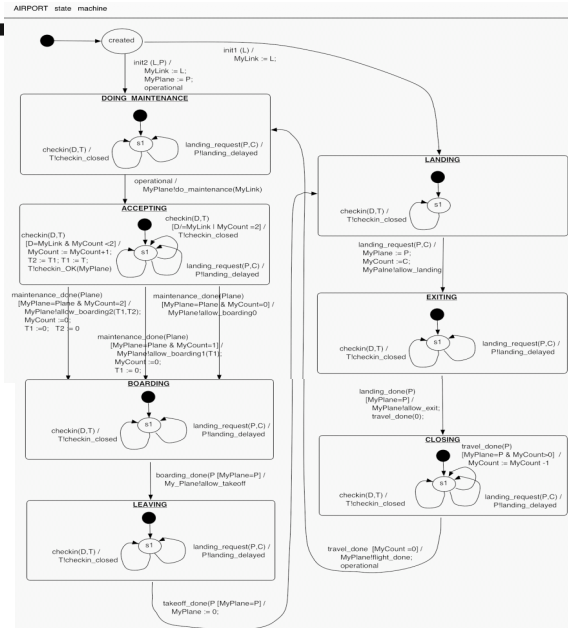


### UMC and the airport case study



F

# UMC and the airport case study

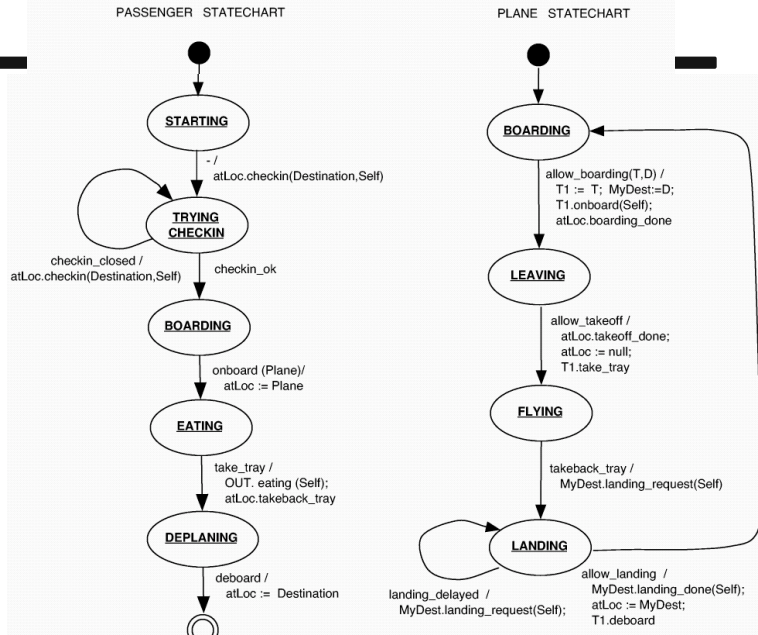


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# UMC and the airport case study



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## Proving properties

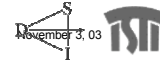
We want to check if it is true that passengers can eat, only when their plane is flying  
 (we have only one plane in the model "Plane1").  
 This can be done by checking the following formula:

```
AG ( ( EX { eating } true ) ->
  ASSERT(Plane1.Status=1) )
```

The formula:

```
max Z : < ( ~ eating ) & ~ checkin_closed > Z
```

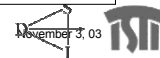
The above formula, is true. I.e. there is some infinite path in which nobody ever eats, and no check-in request are denied.

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## Airport case study

Let us consider one of the simplest scenarios, constituted by two airports (Airport1, Airport2), two Passenger (Traveler1, initially located at Airport1, and Traveler2, initially located at Airport2), and a single Plane (Plane1) traveling between Airport1 and Airport2.

The system is composed by 5 objects, and originates a LTS containing 18131 states and 55379 transitions.

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## Open research issues

Another interesting research issue is the study of counterexamples: one area of interest about counterexamples is given by the possibility of generating test cases from them; due to the common practice that sees verification of hardware and software ecomponents often effectively carried on by testing, can we use counterexample to enhance testing coverage?

Particularly interesting in this sense is the possibility to abandon simple **linear counterexample** in favour of **tree-like counterexamples** or **counterexample autoamata**.

## References – Model checking classics

- E. M. Clarke and E. A. Emerson. Synthesis of synchronization skeletons for branching time temporal logic. In *Logic of programs: workshop, Yorktown Heights, NY, May 1981*, volume 131 of *Lecture Notes in Computer Science*. Springer-Verlag.
- J.P. Quielle and J. Sifakis. Specification and verification of concurrent systems in CESAR. In *Proceedings of the Fifth International Symposium in Programming*, volume 137 of *Lecture Notes in Computer Science*. Springer-Verlag, 1981.
- E. M. Clarke, E. A. Emerson, and A. P. Sistla. Automatic verification of finite-state concurrent systems using temporal logic specifications. *ACM Trans. Programming Languages and Systems*, 8(2):pages 244–263, 1986.
- E. M. Clarke, O. Grunberg, D. A. Pele, *Model Checking*, MIT Press, 1999
- J. R. Burch, E. M. Clarke, K. L. McMillan, D. L. Dill, and J. Hwang. Symbolic model checking: 10<sup>20</sup> states and beyond. *Information and Computation*, 98(2):pages 142–170, 1992. K. L. McMillan. *Symbolic Model Checking*. Kluwer Academic Publishers, 1993.

## References – Action based Model checking

- De Nicola R, Vaandrager F.W. Actions versus State Based Logics for Transition Systems. Proc. Ecole de Printemps on Semantics of Concurrency, Lecture Notes in Computer Science vol. 469, 1990, 407-419.
- R. De Nicola, A. Fantechi, S. Gnesi, G. Ristori, "An action based framework for verifying logical and behavioural properties of concurrent systems", Computer Networks and ISDN Systems, Vol. 25, N. 7, pp. 761-778, North Holland, Febbraio 1993.
- C. Bernardeschi, A. Fantechi, S. Gnesi, S. Larosa, G. Mongardi, D. Romano, "Formal Verification Environment for Railway Signalling System Design", Formal Methods in System Design, Vol. 12, pp.139-161, 1998.
- S. Gnesi, G. Ristori. A Model Checking Algorithm for  $\mu$ -calculus agents. In Advances in Temporal Logic, H. Barringer, M. Fisher, D. Gabbay, G. Gough eds., Applied Logic Series, Vol. 16, Kluwer Academic Publishers, 2000, pp.339-358.
- A. Fantechi, S. Gnesi, F. Mazzanti, R. Pugliese, E. Tronci A Symbolic Model Checker for ACTL, Applied Formal Methods -- FM-Trends 98, LNCS 1641, Springer - Verlag, 1999.
- S. Gnesi and F. Mazzanti, On the Fly Verification of Networks of Automata, International Conference on Parallel and Distributed Processing Techniques and Applications (PDPTA'99), special session on Current limits to automated verification for distributed systems, CSREA Press, 1999 (Invited paper).
- G. Ferrari, S. Gnesi, U. Montanari, M. Pistore, G. Ristori Verifying Mobile Processes in the HAL Environment, CAV'98, LNCS 1427, Springer - Verlag, 1998.
- C. Bernardeschi, A. Fantechi, S. Gnesi, "Formal Validation of Fault-tolerance Mechanisms inside GUARDS", Journal of Reliability Engineering and System Safety, Vol. 71, n.3, Feb. 2001, pp. 261-270.
- N. De Francesco, A. Fantechi, S. Gnesi, P. Inverardi, "Finite Approximations for Model Checking Non-finite-state Processes", The Computer Journal, vol 44 n. 2, 2001.
- R. Meolic, T. Kapus, Z. Brezocnik, "An Action Computation Tree Logic With Unless Operator", SEEFM 2003, Thessaloniki, Greece, November 20, 2003.
- M. Hennessy, R. Milner, "Algebraic Laws for Nondeterminism and Concurrency", Journal of ACM, vol. 32, n. 1, January 1985, pp. 137-161.
- R. Mateescu, M. Sighireanu Efficient On-the-Fly Model-Checking for Regular Alternation-Free  $\mu$ -Calculus Proceedings of the 5th International Workshop on Formal Methods for Industrial Critical Systems FMICS'2000 (Berlin, Germany), April 2000
- A. Bouali, S. Gnesi, S. Larosa S. The integration Project for the JACK Environment. Bulletin of the EATCS, 54, 1994, 207-223.